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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Pendharker et al.

Docket Number: TI-33654

Serial No.: 10/678,028

Art Unit: 2814

Filed: September 29, 2003

Examiner: T. Doan

For: A LOW NOISE VERTICAL VARIABLE GATE CONTROL VOLTAGE

JFET...

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Pendharker et al. TITLE OF INVENTION: A Low Noise Vertical Variable Gate		Serial No.: 10/678,028 Filling Date: 09/29/2003
TI-33654	DEPOSIT ACCT. NO.: 20-0668	
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE PATENT APPLICATION

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Examiner: T. Doan

For:

A LOW NOISE VERTICAL VARIABLE GATE CONTROL VOLTAGE JFET DEVICE IN

A BICMOS PROCESS AND METHODS TO BUILD THIS DEVICE

CERTIFICATE OF MAILING OR TRANSMISSION

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EXTENSION OF TIME

Sir:

Pursuant to 37 CFR 1.136(a), Applicant(s) respectfully petitions the Commissioner for an extension of the shortened statutory period in the above-referenced application.

The fee for this extension is indicated below:

One Month (\$120)

Please charge the fee to Deposit Account No. 20-0668. Any further necessary extension of time is hereby requested. Charge any additional fee or credit any overpayment to Deposit Account No. 20-0668.

Respectfully submitted.

P. O. Box 655474 MS 3999 Dallas, TX 75265

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